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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/583,530	06/01/2000	Naoyuki Taguchi	018901/0174	5064

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WASHINGTON, DC 20007

EXAMINER

NGUYEN, DUNG T

ART UNIT PAPER NUMBER

2871

DATE MAILED: 07/16/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/583,530

Applicant(s)

TAGUCHI, NAOYUKI

Examiner

Dung Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☐ Responsive to communication(s) filed on 01 June 2000.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) 10 and 11 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☐ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) 9 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 01 June 2000 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All b) ☐ Some \* c) ☐ None of:  
1. ☒ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 3-5.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

## **DETAILED ACTION**

### ***Election/Restrictions***

1. Applicant's election without traverse of Group I (claims 1-9) in Paper No. 8 (dated 04/26/2003) is acknowledged.

### ***Priority***

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

### ***Drawings***

3. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference characters "5" and "8" have both been used to designate drain layers. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.
4. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference characters "7" and "9" have both been used to designate pixel electrode. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

### ***Specification***

5. The disclosure is objected to because of the following informalities: "source layers 9" should be changed to --source layers 7--.

Appropriate correction is required.

***Claim Rejections - 35 USC § 102***

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

7. Claims 1-8 are rejected under 35 U.S.C. 102(a) as being anticipated by Applicant's admitted prior art (APA), figures 1-3.

The above claims are anticipated by APA's figures 1-3 which disclose a liquid crystal display (LCD) device comprising:

- . a substrate (1) having a major surface;
- . an array of pixels arranged in a delta pattern (see figure 1);
- . a thin film transistor (TFT) having
  - . gate layers (2);
  - . a gate insulating layer (3) of SiN and SiO (see application, page 2);
  - . an amorphous silicon layer (4), wherein the amorphous silicon layer together with the gate insulating layers (figure 2);
  - . source layers (7) formed in contact with the amorphous silicon layer (figure 2);and
  - . drain layers (8) formed in contact with the amorphous silicon layer and spaced from the source layer and the source layer of the adjacent TFTs by first regions of the

gate insulating layer (regions between two TFTs), wherein the drain layers have portions extending in parallel to portions of the source layers of the adjacent TFTs, and the gate layers (portion of the drain layers aligned in the horizontal direction) (figure 2) and liable to be short circuited with the portions of the drain layer due to pieces of residual amorphous silicon (no residual amorphous silicon in the pattern, specification, page 3, line 8). Furthermore, the source layers and the drain layers formed of a conductive material (e.g. alloy);

- . a plurality of pixel electrodes (9) connected to the source layer (7) (figure 2);
- . storage electrode layers (12) paralleled to and spaced from the gate layers by second regions of the gate insulating layer (regions between storage electrodes and gate layers)(figure 1);
- . contact slits (6) extending through the first regions and the second regions so as to expose parts of the major surface under the second regions (see figures 2 and 3).

***Allowable Subject Matter***

8. Claim 9 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

9. The following is a statement of reasons for the indication of allowable subject matter:

None of the prior art of record discloses or suggests alone or in combination that a liquid crystal display in which the contact slits extends through a part of the gate insulating layer between one of the gate layers and adjacent one of the plural drain layers, another part of the gate

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insulating layer between said adjacent one of said plural drain layers and adjacent one of said plural storage electrode layers and yet another part of the gate insulating layer between one end portion of said part of the gate insulating layer and one end portion of another part of said gate insulating layer opposed to said one end portion of said part as set forth in claim 9.

### *Conclusion*

10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dung Nguyen whose telephone number is 703-305-0423. The examiner can normally be reached on Monday-Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert H. Kim can be reached on 703-305-3492. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7726 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.



DN  
July 14, 2003

**Dung Nguyen**  
**Patent Examiner**  
**Art Unit 2871**